

IL920020007US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of: **Alon et al**

Serial No.: 10/091,934

Filed: March 06, 2002

Confirmation No.: 7058

Title: AN INTERCONNECT-AWARE METHODOLOGY FOR INTEGRATED CIRCUIT DESIGN

Group Art Unit: 2825

Examiner: Naum B Levin

Date: January 23, 2004

Honorable Commissioner for Patents

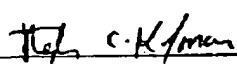
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*See  
Only*

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this paper (Transmittal Letter, One month Extension of Time, Non Fee Amendment Transmittal 10 pages, Amendment and Remarks, 4 pages signed Declaration and 19 pages, Appendix A) is being facsimile transmitted under Rule 37 CFR 1.6(d) to the U.S. Patent and Trademark Office to (703) 872-9306 on January 23, 2004.

  
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Stephen C. Kaufman  
Registration No.: 29,551  
Tel. (914) 945-3197  
Fax (914) 945-3281

Amendment

Sir:

This amendment is filed in response to the Office Action dated 16 October

2003 issued by the USPTO in connection with the above-identified patent application.

The listing of claims begins on page 2 of this paper.

Remarks begin on page 7 of this paper.

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an optional step described in steps 16 and 17 (column 20, lines 10 – 14 and 18 – 26), Fig. 5 and item 712 of figure 7 (column 22, lines 38 – 38). Dangelo et al. further discusses wire delays in items 808 and 812 of Fig. 8 (column 23, lines 23 – 42 and 52 – 61). Dangelo et al. describes very rough and approximate wiring effects considerations sufficient for 1994, but it does not discuss, and is not even aware of, the need to address critical on-chip interconnects using transmission line topologies. Thus Dangelo et al. does not suggest or teach the "designing one or more transmission line topologies"; it has no incentive to.

Claims 26 and 27 were rejected under 35 U.S.C. 103(a) as being unpatentable over Dangelo in view of Chang. In view of the declaration filed under 37 CFR 1.131, applicants respectfully believe that these rejections are moot as well.

Applicants believe that the above amendments and remarks are fully responsive to all the objections and grounds of rejections by the examiner. In view of the foregoing amendments and remarks, the applicants respectfully submit that all the pending claims are deemed to be allowable. Their favorable reconsideration and allowance is respectfully requested.

Should the Examiner have any question or comment as to the form, content or entry of this Amendment, the Examiner is requested to contact the undersigned at the telephone number below. Similarly, if there are any further issues yet to be resolved to advance the prosecution of this application to issue, the Examiner is requested to telephone the undersigned counsel.

Please charge any fee associated with this paper to deposit account No. 09-0468.

Respectfully submitted,

By: Stephen C. Kaufman

Stephen C. Kaufman  
Attorney for Applicant  
Registration No. 29,551

IBM Corporation  
Intellectual Property Law Department  
P. O. Box 218  
Yorktown Heights, New York 10598  
Telephone No.: (914) 945-3197

Serial No. 10/091,934

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: : Date: January 23, 2004  
Alon et al. : Confirmation No.: 7058  
Serial No.: 10/091,934 : Group Art Unit: 2825  
Filed: March 06, 2002 : Examiner: Naum B. Levin

Title: AN INTERCONNECT-AWARE METHODOLOGY FOR INTEGRATED CIRCUIT DESIGN  
ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

PETITION AND FEE FOR EXTENSION OF TIME (37 CFR 1.136(a))

1. This is a petition for an extension of time to respond to the Office Action mailed on October 16, 2003, for a period of one (1) month from January 16, 2003, to and including January 23, 2003.
2. Applicant is other than a small entity.
3. Extension:

Month(s)

Estimated fee for other  
than Small Entity

<input checked="" type="checkbox"/> one month	\$ 110.00
<input type="checkbox"/> two months	\$ 410.00
<input type="checkbox"/> three months	\$ 930.00
<input type="checkbox"/> four months	\$ 1,450.00

FEE: \$ 110.00

4. An Amendment/Response

☒ is filed herewith  
☐ has been filed.

5. Fee payment

☒ Charge the required fee to Deposit Account No. 09-0468 and charge any additional fee required or credit for any excess fee paid to that account. A duplicate of this petition is attached.

Respectfully submitted,

By:

Stephen C. Kaufman  
Attorney: Stephen C. Kaufman  
Reg. No. 29,551  
Telephone No. (914) 945-3197

IBM Corporation  
Intellectual Property Law Department  
P.O. Box 218  
Yorktown Heights, New York 10598

Attorney Docket No.: IL920020007US1